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Attorney Docket No. 015114-069200US

Client Ref. A1256

STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Ketan Padalia et al.Application No./Patent No.: 10/716,309 Filed/Issue Date: November 17, 2003Entitled: Techniques for Grouping Circuit Elements into Logic BlocksAltera Corporation a Corporation

(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

- the assignee of the entire right, title, and interest; or
- an assignee of an undivided part interest

in the patent application/patent identified above by virtue of either:

A. An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the Patent and Trademark Office at Reel 014549 Frame 0164, or for which a copy thereof is attached.

OR

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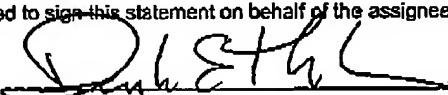
Copies of assignments or other documents in the chain of title are attached.

[NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.8]

The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the assignee.

11/5/03

Date



Signature

Derek E. Minihane

Typed or printed name

Director, Intellectual Property Law
and Assistant Secretary

Title

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